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EFS ID:	6858463			
Application Number:	09051263			
International Application Number:				
Confirmation Number:	7818			
Title of Invention:	RISC MICROPROCESSOR ARCHITECTURE			
First Named Inventor/Applicant Name:	GEORGE W. SHAW			
Customer Number:	40972			
Filer:	Larry E. Henneman			
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Receipt Date:	21-JAN-2010			
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Warnings:					
Information	:				
Request for Co	Request for Continued Examination	RCE.pdf	87097	no	1
2	(RCE)	nct.pai	9101078de789354cf2ddace6c9af004bf796 db17	110	'
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	Amendment Submitted/Entered with	Amendment.pdf	113688	no	12
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.